







FIG. 4

# TESTING CIRCUIT

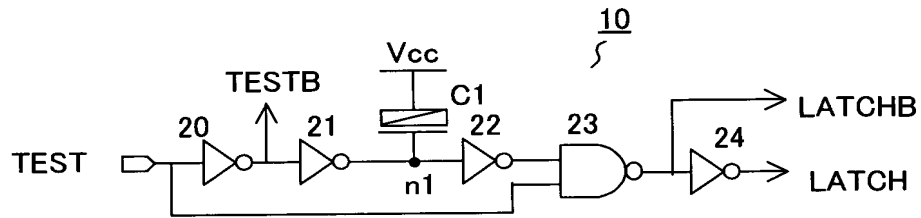


FIG. 5

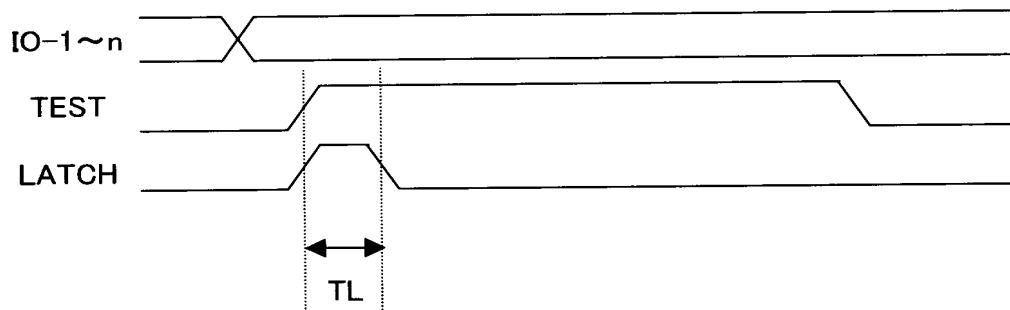


FIG. 6

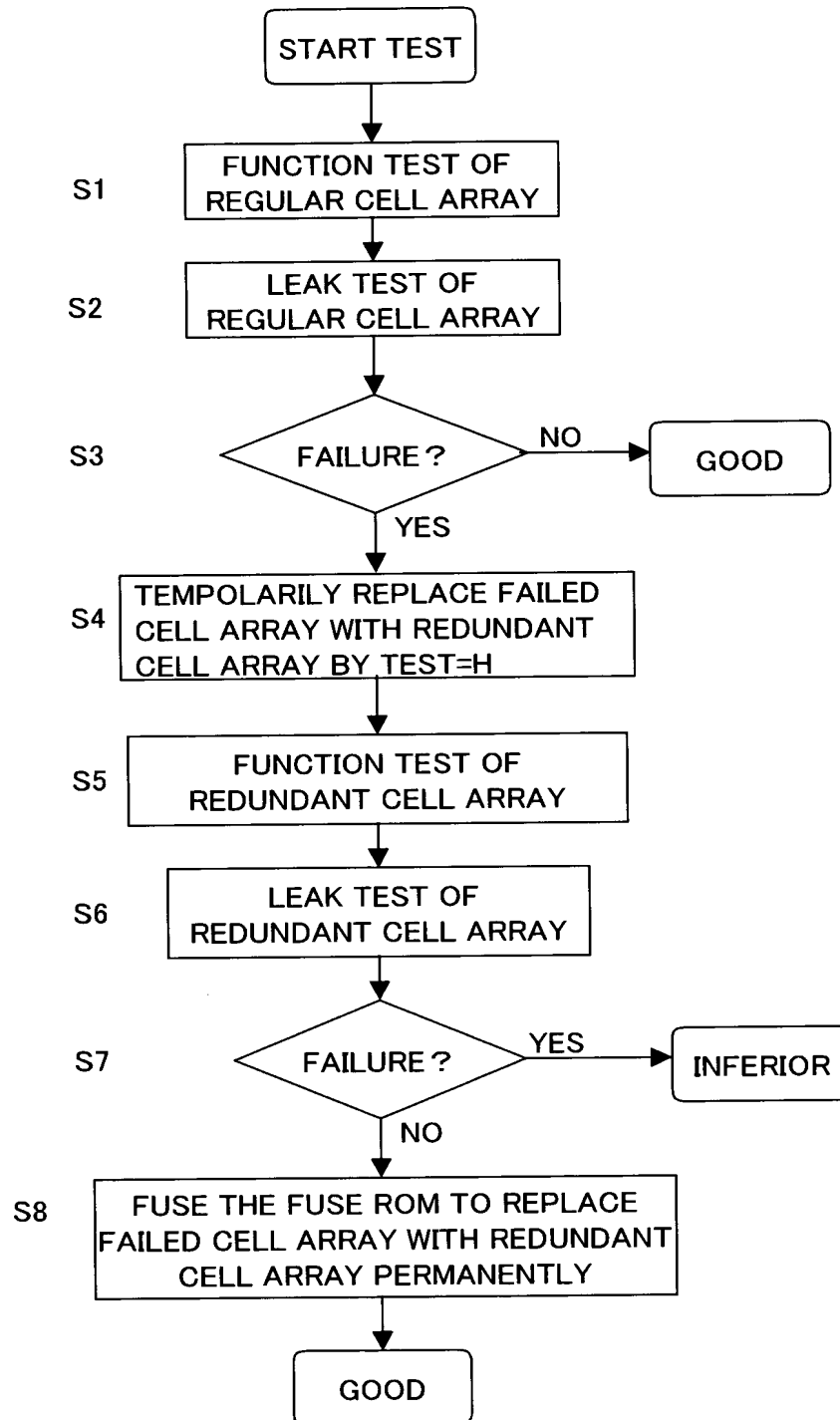




FIG. 8

REDUNDANT REPLACEMENT MEMORY

